GLOBAL INSTITUTE OFENGINEERING&TECHNOLOGY
(Approved by AICTE & Affiliated to JNTUH)
Survey No. 179, Chilkur (V), Moinabad (M), Ranga Reddy Dist. TS.
Phone: 8790101015 / 9959250205
e-mail: principal.giet.u6@gmail.com
JNTUH Code(U6) CIVIL – CSE – MECH – ECE – EEE – MBA – M.Tech. EAMCET Code– GLOB
Department of Electronics and Communication Engineering

Date: 12/07/2017

CIRCULAR

All the students of II year of the Department of ECE are informed to express their interest by enrolling their name for the One week Bridge course on **"Analog Electronics"** starting from 24/07/2017 to 29/07/2017 .

The detailed syllabus for the course is attached for your information. Concerned mentors are instructed to submit the list of students enrolled within two days to the undersigned. For further information, you can contact the Course Coordinator Mrs. Nuzath Unnisa or Head of the Department.

12/7/2017
Coordinator

Cc to:
Principal
Coordinator IQAC
Mentors
Training & Placement Cell
Notice board
File

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Department of Electronics and Communication Engineering

G Ahmed Zeeshan
BE.,M.Tech.,MBA.,(Ph.D)
MISTE.,MISOI.,MIEEE(USA)
Assistant Professor & Head

Lr.No: ECE/B.Tech/SA/BC-AE/2017-01

Date 12/07/2017

To
The Principal
GIET

Sir,

Sub: Hosting one week Bridge Course on 'Analog Electronics'– Reg.

*****

The Department of Electronics and Communication Engineering is planning to host one week Bridge Course on 'Analog Electronics' from 24/07/2017 to 29/07/2017 in Room No. 04 for B.Tech II years (A.Y.2017-2018). It is mandatory for the students to learn 'Analog Electronics' as it is one of the core subjects. Also, as it is a bit complex Concept it requires a Bridge Course. I would like to request for your permission and support to conduct the Course. It will be ensured that information delivered to students is standardized.

The objectives of the course are to enable the participating students to understand the concepts of 'Analog Electronics' which leads to new and creative ideas. It is therefore important for students to have a deep understanding of concept and to enhance their knowledge. This Bridge Course provides the required and basic knowledge on the contents of Electronics, which is one of the two major streams of ECE.

Kindly accord approval so as to make necessary arrangements for hosting one week Bridge Course on 'Analog Electronics'
Thanking you.

12/07/2017
HEAD

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Department of Electronics and Communication Engineering

COURSE SYLLABUS FOR 'ANALOG ELECTRONICS'

S.no	Topics to delivered	Duration (in hours)
1	Introduction to semiconductor components	3
2	Semiconductor Devices	3
3	PN junction diodes	3
4	Transistor and its modes of operations	3
5	Common Base configuration	3
6	Common Emitter configuration	3
7	FET Amplifiers	3
8	Classification of FET Amplifiers	3
9	Feedback In Amplifiers	3
10	Types of Feedback In Amplifiers	3
11	Large Signal Amplifiers	3

13/7/2017

Coordinator

13/07/2017

HEAD

Department of Electronics & Communication Engg
Global Institute of Engineering & Technology
Chilkur (V)., Moinabad (M)., R.R. Dist.T.S.-501504.

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EAMCET Code– GLOB
Department of Electronics and Communication Engineering

LIST OF STUDENTS ENROLLED FOR ANALOG ELECTRONICS
2017-2018
LIST OF PARTICIPANTS

S.no	Roll no	Name of the student	Year	Dept
1	15U61A0427	Vem Vamshi	II	ECE
2	16U61A0401	Afreen Begum	II	ECE
3	16U61A0403	Asra Fardeen	II	ECE
4	16U61A0405	Baddamolla Shirisha	II	ECE
5	16U61A0407	B.Karthik Kumar Reddy	II	ECE
6	16U61A0409	Ch Revanth	II	ECE
7	16U61A0410	D.Bhargav Sai	II	ECE
8	16U61A0413	J.Vikas Kumar	II	ECE
9	16U61A0414	Kauser Begum	II	ECE
10	16U61A0418	Mohd Kazim Sharif	II	ECE
11	14U61A0419	N.Sowmya	II	ECE
12	16U61A0420	N.Pooja Reddy	II	ECE
13	16U61A0421	N.Narendra	II	ECE
14	16U61A0422	Pandaoula Shashikumar	II	ECE
15	16U61A0424	Sheelam Ramakrishna	II	ECE
16	16U61A0425	Summaiya	II	ECE
17	16U61A0426	Yanagandula Suresh	II	ECE

24/7/2017
Coordinator

24/07/2017
HEAD
Department of Electronics & Communication Engg.
Global Institute of Engineering & Technology
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Department of Electronics and Communication Engineering

One Week Bridge Course
On
Analog Electronics
(24th to 29th July, 2017)

PROGRAMME SCHEDULE

	Forenoon		Afternoon
Day1	**Inauguration**	**Introduction to semiconductor components** *Mr.G Ahmed Zeeshan, Asst.prof&Head,Dept of ECE*	**Semiconductor Devices** *Mr.G Ahmed Zeeshan, Asst.prof&Head,Dept of ECE*
Day2	**PN junction diodes** *Dr. C Shyam Anand Professor, Dept of ECE,GIET*		**Transistor and its modes of operations** *Dr. C Shyam Anand Professor, Dept of ECE,GIET*
Day3	**Common Base configuration** *Mr.G Ahmed Zeeshan, Asst.prof&Head,Dept of ECE*		**Common Emitter configuration** *Mrs. Ishrath Unisa Assistant Professor, Dept of ECE, GIET*
Day4	**FET Amplifiers** *Mrs. Ishrath Unisa Assistant Professor, Dept of ECE, GIET*		**Classification of FET Amplifiers** *Mrs Nuzath Unnisa Assistant Professor, Dept of ECE, GIET*
Day5	**Feedback In Amplifiers** *Mrs Nuzath Unnisa Assistant Professor, Dept of ECE, GIET*		**Types of Feedback In Amplifiers** *Ms. M. Sai Chandana Assistant Professor, Dept of ECE, GIET*
Day6	**Large Signal Amplifiers** *Ms. Sana Fathima Assistant Professor, Dept of ECE, GIET*		**Valedictory& Feedback**

24/7/2017

Coordinator
Mrs. Nuzath Unnisa
Assistant Professor
Department of ECE

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One Week Bridge Course
On
Analog Electronics
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ATTENDANCE SHEET

S.No	Name of the Participant	Programme	Year	SIGNATURE											
				Day-1 (24/07/2017)		Day-2 (25/07/2017)		Day-3 (26/07/2017)		Day-4 (27/07/2017)		Day-5 (28/07/2017)		Day-6 (29/07/2017)	
				FN	AN	FN	AN	FN	AN	FN	AN	FN	AN	FN	AN
1	Vem Vamshi	B.Tech	II	← Ab →											
2	Afreen Begum	B.Tech	II												
3	Asra Fardeen	B.Tech	II									← Ab →			
4	Baddamolla Shirisha	B.Tech	II												
5	B.Karthik Kumar Reddy	B.Tech	II			← Ab →									

Coordinator 29/7/2017

Chilkur (V). Moinabad (M). R.R. Dist. (T.S.) CC-U6

29/07/2017
Head of the Department
Department of Electronics & Communication Engg.
Global Institute of Engineering & Technology
Chilkur (V)., Moinabad (M)., R.R. Dist.T.S.-501504

PRINCIPAL
Global Institute of Engg. & Tech
Chilkur (V), Moinabad (M)
R.R. Dist
Principal

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				FN	AN	FN	AN	FN	AN	FN	AN	FN	AN	FN	AN
6	Ch Revanth	B.Tech	II	Rev	Rev	Rev	Rev	Rev	Rev	Rev	Rev	Rev	Rev	← Ab →	
7	D.Bhargav Sai	B.Tech	II	D.Bhg	D.Bh	← Ab →		D.Bh	D.Bh	D.Bh	D.Bh	D.Bh	D.Bh	D.Bh	D.Bh
8	J.Vikas Kumar	B.Tech	II	Vikas	Vikas	Vikas	Vikas	Vikas	Vikas	Vikas	Vikas	← Ab →		Vikas	Vikas
9	Kauser Begum	B.Tech	II	Kauser	Kauser	Kauser	Kauser	Kauser	Kauser	Kauser	Kauser	Kauser	Kauser	Kauser	Kauser
10	Mohd Kazim Sharif	B.Tech	II	Kf	Kf	Kf	Kf	Kf	Kf	Kf	Kf	Kf	Kf	Kf	Kf

29/7/2017
Coordinator

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Chilkur (V), Moinabad (M), R.R. Dist. (T.S.)
CC-U6

29/07/2017
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29/7/17
Principal
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				Day-1 (24/07/2017)		Day-2 (25/07/2017)		Day-3 (26/07/2017)		Day-4 (27/07/2017)		Day-5 (28/07/2017)		Day-6 (29/07/2017)	
				FN	AN	FN	AN	FN	AN	FN	AN	FN	AN	FN	AN
11	N.Sowmya	B.Tech	II	N.S.R	N.S.R	← Ab →		N.S.R	N.S.R	N.S.R	N.S.R	N.S.R	N.S.R	N.S.R	N.S.R
12	N.Pooja Reddy	B.Tech	II	N.Pooja	N.Pooja	N.Pooja	N.Pooja	N.Pooja	N.Pooja	N.Pooja	N.Pooja	N.Pooja	N.Pooja	N.Pooja	N.Pooja
13	N.Narendra	B.Tech	II	Naf	Naf	Naf	Naf	Naf	Naf	← Ab →		Naf	Naf	Naf	Naf
14	Pandaoula Shashikumar	B.Tech	II	Shashi	Shashi	Shashi	Shashi	Shashi	Shashi	← Ab →		Shashi	Shashi	Shashi	Shashi
15	Sheelam Ramakrishna	B.Tech	II	ele	ele	ele	ele	ele	ele	ele	ele	ele	ele	ele	ele

29/7/2017
Coordinator

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29/07/2017
HEAD
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Head of the Department

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				Day-1 (24/07/2017)		Day-2 (25/07/2017)		Day-3 (26/07/2017)		Day-4 (27/07/2017)		Day-5 (28/07/2017)		Day-6 (29/07/2017)	
				FN	AN	FN	AN	FN	AN	FN	AN	FN	AN	FN	AN
16	Summaiya	B.Tech	II	Summaiya	Summaiya	Summaiya	Summaiya	Summaiya	Summaiya	Summaiya	Summaiya	Summaiya	Summaiya	Summaiya	Summaiya
17	Yanagandula Suresh	B.Tech	II	Ys	Ys	Ys	Ys	← Ab →		Ys	Ys	Ys	Ys	Ys	Ys

29/7/2017
Coordinator

Chilkur (V), Moinabad (M), R.R. Dist. (T.S.) CC-U6 GLOBAL INSTITUTE OF ENGINEERING & TECHNOLOGY

29/07/2017
Head of the Department
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On
Analog Electronics
(24th-29th July,2017)

FEEDBACK FORM

Please valuate your rating of the course by placing a tick in the appropriate box.
1. Poor 2. Satisfactory 3. Good 4. Very good 5. Excellent

Branch and Year: ECE D year **Date:** 29/07/2017

ASPECTS	RATING				
	Excellent 5	Very good 4	Good 3	Satisfactory 2	Poor 1
Relevance of contents	✓				
Trainer was knowledgeable and skillful	✓				
Quality of input provided	✓				
Quality of presentations		✓			
Adherence to the time schedule					
Opportunity given to participant to clear doubts	✓				
Identify ways to build on current skills and knowledge		✓			
Overall learning experience	✓				

How has the course enhanced your skills or understanding of this topic?
we came to understand the basics of Electronics very clearly. It is very good and we are looking forward for more such courses.

Specify problems faced by you during the course?

Any Other Comments:

REPORT

ONE WEEK BRIDGE COURSE
ON
ANALOG ELECTRONICS

One Week Bridge course on 'Analog Electronics' was organized by the Department of Electronics and Communication Engineering. This course was held in the Conference Room of GIET from 24th to 29th July, 2017 and its objective was to further enhance and strengthen the technical skills of the students in the Electronic devices in ECE. The course was attended by 17 students of Electronics and Communication Engineering Department (A.Y. 2017-18). Evaluation forms were also filled by each participant at the end of the course.

Day 1:

The course started off with the inaugural ceremony and the Coordinator of this occasion, Mrs Nuzath Unnisa, Assistant Professor, Department of ECE welcomed the gathering. Later in the inauguration Mr.G. Ahmed Zeeshan, Asst.prof&Head, Dept of ECE delivered his inaugural and introductory speech at 10.00 AM in which his energetic talk was on 'Introduction to semiconductor components'. He explained students about the basic components, like resistors, capacitors, inductors and their units. Also sir explained the color coding of all these basic components. In the afternoon session he emphasized with a brief lecture on the 'Semiconductor Devices'. In this he highlighted on different semiconductor devices like diodes and transistors.

Day 2:

On Second day, forenoon session was taken by Dr. C Shyam Anand Professor, Dept of ECE, GIET, in which he delivered a lecture on 'PN junction diodes'. In this session he explained students that, A p–n junction is a boundary or interface between two types of semiconductor materials, p-type and n-type, inside a single crystal of semiconductor. The "p" (positive) side contains an excess of holes, while the "n" (negative) side contains an excess of electrons in the outer shells of the electrically neutral atoms there. This allows electrical current to pass through the junction only in one direction. In the afternoon session Dr. C Shyam Anand Professor, Dept of ECE, GIET, delivered his lecture On 'Transistor and its modes of operations' like, in cutoff mode there is no current flow in transistor. In other words the base-emitter as well as the base-collector junctions are reverse biased. The transistor works just like an open circuit. In active mode B-E junction is forward biased and B-C junction is reversed biased. In this mode transistor behaves like a closed switch. In saturation mode the maximum

amount of current passes through the transistor. For this both B-E junction and B-C junction should be forward biased.

Day 3:

On third day, forenoon session was on 'Common Base configuration' by Mr. G Ahmed Zeeshan, Asst.prof&Head, Dept of ECE,GIET, in which he spoke that, for the common base configuration to operate as an amplifier, the input signal is applied to the emitter terminal and the output is taken from the collector terminal. Thus the emitter current is also the input current, and the collector current is also the output current, but as the transistor is a three layer, two pn-junction device, it must be correctly biased for it to work as a common base amplifier. That is the base-emitter junction is forward-biased. In the afternoon session Mrs. Ishrath Unisa, Assistant Professor, Dept of ECE, GIET lectured on 'Common Emitter configuration'. This included the topics like, in electronics, a common-emitter amplifier is one of three basic single-stage bipolar-junction-transistor (BJT) amplifier topologies, typically used as the voltage amplifier. In this circuit the base terminal of the transistor serves as the input, the collector is the output, and the emitter is common to both.

Day 4:

On fourth day, forenoon session was a lecture on 'FET Amplifiers' by Mrs. Ishrath Unisa, Assistant Professor, Dept of ECE, GIET, in which she said that a FET amplifier is an amplifier that uses one or more field-effect transistors (FETs). The most common type of FET amplifier is the MOSFET amplifier, which uses metal–oxide–semiconductor FETs (MOSFETs). The main advantage of a FET used for amplification is that it has very high input impedance and low output impedance. The next session was a lecture on 'Classification of FET Amplifiers' by Mrs Nuzath UnnisaAssistant Professor, Dept of ECE, GIET in which she said that, The Junction FET transistor is a type of field effect transistor that can be used as an electrically controlled switch MOSFET transistor as its name suggests is a p type (n type) semiconductor bar (with two heavily doped n type regions diffused into it) with a metal oxide layer deposited on its surface and holes taken out of the layer to form source and drain terminals.

Day 5:

On fifth day the forenoon session started with a lecture on 'Feedback In Amplifiers' by Mrs Nuzath UnnisaAssistant Professor, Dept of ECE, GIET.In this she highlighted the concepts like An amplifier circuit simply increases the signal strength. But while amplifying, it just increases the strength of its input signal whether it contains information or some noise along with information. The next session was

by Ms. M. Sai Chandana, Assistant Professor, Dept of ECE, GIET on 'Types of Feedbacks In Amplifiers', in which she said that There are four basic ways of connecting the feedback signal. Both voltage and current can be feedback to the input either in series or parallel. Specifically there can be Voltage Series Voltage shunt, current series and current shunt.

Day 6:

The final day forenoon session was a lecture on 'Large Signal Amplifier' by Ms. Sana Fathima Assistant Professor, Dept of ECE, GIET, in which she said that, Large Signal Amplifier Power Amplifier: In a multistage amplifier, first stage or the input stage and the intermediate stage are usually small signal class A amplifier stage while the last stage (the output stage) or sometimes last two stage are large signal amplifier (power amplifier) stage. Small signal amplifier stages serve to amplify the weak input signal to a sufficient large value to drive the final stage. Valediction was conducted in the afternoon session. Feedback for the course was taken from the students. Participation Certificates were also given for all the students.

29/7/2017

Coordinator
Mrs. Nuzath Unnisa
Assistant Professor
Department of ECE